

Abstract

5 The invention relates to an electronic circuit for decoding
a read signal from an optical storage medium, the
electronic circuit comprising a limit equalizer for
amplification of high frequency read signal components
without substantially increasing inter-symbol interference,
10 a Viterbi detector being coupled to an output of the limit
equalizer for providing the decoded read signal, a
coefficient adaptation circuit for tuning the limit
equalizer to a partial response target.

Figures 1 to 7